FORM PTO-1449 (MODIFIED)

LIST OF PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT



Applicant(s): Stephen V. Kosonocky

Docket No.: Y0999-369 Serial No.: 09/589,716 Filing Date: June 8, 2000

Group: 2124

U.S. PATENT DOCUMENTS					
EXAMINER INITIAL	DOCUMENT NO.	DATE	NAME	CLASS/SUBCLASS	FILING DATE IF APPROPRIATE
EXAMINER		F	OREIGN PATENT DOCU	MENTS	TRANSLATION
INITIAL	DOCUMENT NO.	DATE	COUNTRY	CLASS/SUBCLASS	YES NO
			OTHER DOCUMENT	re	
EV A L GATED	•		OTHER DOCUMENT	15	
EXAMINER INITIAL	REF NO.	AUTHOR, TITLE, DA	FE, PERTINENT PAGES, ETC.		

1. R.P. Brent et al., "A Regular Layout for Parallel Adders," IEEE Transactions on Computers, Vol. C-31, No. 3, p. 1-9, March 1982.

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Examiner Date Considered

Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.